Application Number:	10707205					
Filing Date:	26	26-Nov-2003				
Title of Invention:	SYSTEM AND METHOD TO TEST INTEGRATED CIRCUITS ON A WAFER					
First Named Inventor/Applicant Name:	Lawrence Pileggi					
Filer:	Charles L. Moore/Patricia Pontello					
Attorney Docket Number:	361007.000035					
Filed as Large Entity						
Utility Filing Fees						
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)	
Basic Filing:						
Pages:						
Claims:						
Miscellaneous-Filing:						
Petition:						
Patent-Appeals-and-Interference:						
Post-Allowance-and-Post-Issuance:						
Extension-of-Time:						
Extension - 1 month with \$0 paid		1251	1	120	120	

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Total in USD (\$)			120